

**Faculty Name:** K.MAHESH

**Faculty Photo:**



**Faculty Description:**

K. Mahesh is the Assistant Professor, Department of Electronics and Communication Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

## Profile Tab:

### Qualification

Qualification	Institution	Year
Bachelor's degree	Stanley Stephen College of Engineering, JNTUA University	2015
Master's degree	G. Pulla Reddy Engineering College, Kurnool	2018

### Experience

Designation	Institution	From	To
Assistant Professor	Dr. K.V. Subba Reddy College of Engineering	2019	Till Now

### Awards / Achievement

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## Teaching Tab:

### Expertise / list of subjects handled

- Under Graduate Courses:
- Digital Logic Design, Combinational Circuits, Sequential Circuits.
- Digital Signal Processing, DFT, FFT, Algorithms,

- VLSI,
- BEEE.

## Research Tab:

### Research Interest

- VLSI and Mixed Analog Signals.

### Research projects (current)

## Publications tab:

### Books:

### Conferences

1. Attended the National Conference on Recent Innovations in Engineering and Technology NCRIET 2017, G Pullaiah College of Engineering and Technology, National Conference, Kurnool, Andhra Pradesh, 15th - 16th December 2017.

2. Attended the International Conference on Contemporary Engineering and technology (IC CET 2018) will be held at Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering & Technology (PSCMR), Vijayawada, India during April 2 - 3, 2018

### Journals

1. **K. Mahesh**, Dr. S. Govindarajulu, "High Performance Frequency Multiplier for DLL Based Clock Generator in Nano Technology", International Journal of Pure and Applied Mathematics (IJPAM), Volume-118, Issue-20, April 2018, ISSN: 1314-3395.
2. **K.Mahesh**, Design of High Performance 16 Bit Approximation Arithmetic units, Journal of Engineering Sciences ISSN:0377-9254 JES volume10, issue 8 Aug 2019

## Blog Tab:

<https://maheshkumar777.blogspot.com/>