

Faculty Name: P Jaya Rami Reddy

Faculty Photo:



Faculty Description:

Mr. **P Jaya Rami Reddy** is Assistant Professor, Department of Electronics and Communication Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

**Profile Tab:**

Qualification

Qualification	Institution	Year
Bachelor's degree	ALFA COLEGE OF ENGG & TECHNOLOGY,ALLAGADDA	2008
Master's degree	NOVA COLLEGE OF ENGG & TECH, JANGAREDDYGUEDEM	2011

Experience

Designation	Institution	From	To
Assistant Professor	DR KVSRLT,KURNOOL	01.05.2012	Till Date

Awards / Achievement

- A **JNTUA RATIFIED** faculty.
- Member in **Institute of Research Engineers and Doctors** (Membership No. **AM101000583136**)
- LIFE MEMBER in **ISTE** (LM 126771)

## **Responsibilities Tab:**

### Responsibilities

- ✓ Working as a faculty
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### Responsibilities Held

- Teaching regular Course Work
- Mentoring the Students
- Guiding the Junior Faculty
- Continues research

## **Teaching Tab:**

### Expertise / list of subjects handled

- Switching Theory and Logic Design
- Electronic Devices and Circuits
- Electronic Circuit Analysis
- Digital Signal Processing
- Signals and Systems
- Analog Electronic Circuits
- Digital Logic Design
- Analog Communication
- Satellite Communication
- Advanced Digital Signal Processing
- Pulse and Digital Circuits
- Optical fibre communications
  
- **Special Topics:** Laplace Transforms, Flip Flops, Matlab, Multisim & VHDL.

## **Research Tab:**

### Research Interest

Signal Processing

## Publications tab:

### International Journals

1. N Sharath Kumar, **P. Jaya Rami Reddy**, “IMPLEMENTATION OF MONTGOMERY MULTIPLIER BASED HIGH 5-STAGE FIR FILTER”, Journal of Recourse Management and Technology (JRMAT), Vol No 12, Issue 4-2021, November 2018, (ISSN 0745-6999).
2. **P. JayaRami Reddy**, “ UNIFIED FRAME BASED ON COLLABORATIVE REPRESENTATION FOR SINGLE-IMAGE SUPER-RESOLUTION USING CONSISTENT CODING SCHEME”, International Journal of Research (IJR), Vol.VIII, Issue XII, December 2018, (ISSN : 2236-6124)
3. S. Nikitha, **P. Jaya Rami Reddy**, “Area and Energy Efficient 4-T Complementary Dual Modular Redundancy Dynamic Memory Using 45nm CMOS Technology for Space Applications”, Journal of Applied Science and Computations (JASC), Vol No V, Issue No XI, November 2018, (ISSN 1076-5131).
4. **P. Jaya Rami Reddy**, “Reducing Complexity in MIMO Systems with LOPC Coding Using Per-Group Precoding Method”, International Journal of Engineering and Technology Research (IJSETR), Vol No 7, Issue No 7, July 2018, (ISSN 2319-8885).
5. SHAIK SARFARAZ, **P. Jaya Rami Reddy**, “An Efficient Approach to Perform Copy-Move Forgery Detection scheme using Segmentation”, International Journal of Scientific Engineering and Technology Research (IJSETR), Vol No 7, Issue No 7, July 2018, (ISSN 2319-8885).
6. C. Raghavi, **P. Jaya Rami Reddy**, ” Power Efficient Radix-4 Signed-Digit Encoding by Pre-Encoded Multipliers”, International Journal for Research on Electronics and Computer Science (IJRECS), Vol No 10 , Issue No 9, December 2017, (ISSN 2321-5784).
7. K. Venkata Krishnaveni, **P. Jaya Rami Reddy**, “Design and Implementation of FPGA Logic Architectures using Hybrid LUT/Multiplexer”, International Journal of Professional Engineering Studies (IJPRES), Vol No 9, Issue No 4, December 2017.
8. KIRAN KUMAR REDDY. G, **P. Jaya Rami Reddy**, “Interference Alignment of MIMO IC using The Gaussian MIMO XC and the Gaussian MIMO ZC ”, International Journal of Scientific Engineering and Technology Research (IJSETR), Vol No 5, Issue No 23, August 2016, (ISSN 2319-8885).
9. M. Anitha, **P. Jaya Rami Reddy**, “LOW COST SELF TEST TECHNIQUES FOR SMALL RAMs IN SOCs”, International Journal of Engineering and Management Research(IJEMR), Vol No 6, Issue No 1, February 2016, (ISSN 2394-6962).
10. S. Madhu Kamala, **P. Jaya Rami Reddy**, “Design of All-Optical Reversible Logic Circuits Using Novel Optical Reversible Gates”, International Journal & Magazine of Engineering, Technology, Management and Research(IJMETMR), December 2015, (ISSN 2320-3706).
11. K.Mehrajul Haq, **P. Jaya Rami Reddy**, ”A Novel Frame work for Satellite Image Resolution Enhancement Using Wavelet-Domain Approach Based On DT-CWT And NIM”, International Journal of Engineering and Computer Science (IJECS), Vol No 3 , Issue No 12 December 2014, (ISSN 2319-7242).

### Workshops

1. Embedded system design workshop conducted by Edgate Technology Sep 2018

## Blog Tab:

<https://jayaramreddy11.blogspot.com/>